

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	59241	inverter same switch\$3	US-PGPUB; USPAT	2006/09/19 10:52
2	BRS	L2	7696	microcomputer same timer	US-PGPUB; USPAT	2006/09/19 10:52
3	BRS	L3	4460	microcontroller same timer	US-PGPUB; USPAT	2006/09/19 10:53
4	BRS	L4	11921	2 or 3	US-PGPUB; USPAT	2006/09/19 10:53
5	BRS	L5	848	1 and 4	US-PGPUB; USPAT	2006/09/19 10:53
6	BRS	L6	68274	duty adj (cycle ratio)	US-PGPUB; USPAT	2006/09/19 10:54
7	BRS	L7	206	5 and 6	US-PGPUB; USPAT	2006/09/19 10:54
8	BRS	L8	87	mppt	US-PGPUB; USPAT	2006/09/19 10:54
9	BRS	L9	108	maximum adj power adj point adj tracking	US-PGPUB; USPAT	2006/09/19 10:56
10	BRS	L10	128	8 or 9	US-PGPUB; USPAT	2006/09/19 10:57
11	BRS	L11	1	7 and 10	US-PGPUB; USPAT	2006/09/19 11:09
12	BRS	L12	23499	interrupt\$3 adj signal	US-PGPUB; USPAT	2006/09/19 11:10
13	BRS	L13	46346	set\$4 near5 timer	US-PGPUB; USPAT	2006/09/19 11:11
14	BRS	L14	272177	calculat\$3 near5 value	US-PGPUB; USPAT	2006/09/19 11:11
15	BRS	L15	765	13 same 14	US-PGPUB; USPAT	2006/09/19 11:13
16	BRS	L16	527	12 same 13	US-PGPUB; USPAT	2006/09/19 11:13
17	BRS	L17	294	12 same 14	US-PGPUB; USPAT	2006/09/19 11:13
18	BRS	L18	35	16 and 17	US-PGPUB; USPAT	2006/09/19 11:14
19	BRS	L19	15	15 and 18	US-PGPUB; USPAT	2006/09/19 12:28
20	BRS	L20	20	18 not 19	US-PGPUB; USPAT	2006/09/19 12:28